

Notice of References Cited

Application/Control No.

09/683,091

Applicant(s)/Patent Under
Reexamination
CORTI ET AL.

Examiner

Yolanda Wilson

Art Unit

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-2002/0116663 A1	08-2002	Wood et al.	714/30
	B	US-5,964,893 A	10-1999	Circello et al.	714/39
	C	US-6,460,148 B2	10-2002	Veenstra et al.	714/39
	D	US-6,142,683 A	11-2000	Madduri, Venkateswara Rao	717/128
	E	US-US 20030051192A1	03-2003	Pillay et al.	714/39
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Shobaki, Mohammed El and Lennart Lindh. A Hardware and Software Monitor for High-Level System-on-Chip Verification. Marc 26-28, 2001. IEEE International Symposium on Quality Electronic Design. 56-61.
	V	Mosensohn, Guy. Practical Approaches to SOC Verification. July 25, 2001. http://www.verisity.com/download/practical_soc_verification.pdf
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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